



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.Tech II SEMESTER(R13/R16) Regular/Supplementary Examinations, MAY-2017.

College: ADITYA ENGG. COLLEGE, SURAMPALEM, PEDDAPURAM:A9

Discrepancy pertaining to these results are to be submitted on or before 20-12-2017 with following documents at CE(PG) Office,JNTUK,Kakinada

Htno	Subcode	Subname	Internal	External	credits
13A91D0407	H2103	FINITE ELEMENT METHOD	30	25	1
13A91D2102	H2102	ENERGY MANAGEMENT	25	31	1
13A91D2102	H2103	FINITE ELEMENT METHOD	30	24	1
13A91D2102	H2104	COMPUTATIONAL FLUID DYNAMICS	30	8	0
13A91D5203	H4301	SWITCHED MODE POWER CONVERSION	35	38	1
13A91D5506	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	26	20	0
13A91D8708	H8701	FINITE ELEMENT METHOD	30	24	1
13A91D8714	H8701	FINITE ELEMENT METHOD	33	14	0
13A91D8715	H8701	FINITE ELEMENT METHOD	32	-1	0
14A91D0503	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	33	13	0
14A91D0503	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	33	0	0
14A91D0503	H4002	INFORMATION SECURITY	31	2	0
14A91D0807	H0801	ADVANCED NATURAL GAS ENGINEERING	35	10	0
14A91D0816	H0801	ADVANCED NATURAL GAS ENGINEERING	35	0	0
14A91D0816	H0802	ARTIFICIAL LIFT TECHNIQUES	24	0	0
14A91D0816	H0804	INTEGRATED RESERVOIR MANAGEMENT	35	8	0
14A91D0816	H0807	FLOW ASSURANCE	29	0	0
14A91D0816	H0808	PROCESS SAFETY AND ENVIRONMENTAL ASPECTS	28	0	0
14A91D0817	H0802	ARTIFICIAL LIFT TECHNIQUES	33	19	0
14A91D0817	H0803	OPERATIONAL ASPECTS OF WELL TESTING	34	19	0
14A91D0817	H0804	INTEGRATED RESERVOIR MANAGEMENT	38	8	0
14A91D0817	H0807	FLOW ASSURANCE	39	13	0
14A91D2108	H2109	THERMAL MEASUREMENTS AND PROCESS CONTROL	26	18	0
14A91D2109	H2103	FINITE ELEMENT METHOD	27	18	0
14A91D2112	H2109	THERMAL MEASUREMENTS AND PROCESS CONTROL	27	24	1
14A91D2117	H2104	COMPUTATIONAL FLUID DYNAMICS	26	16	0
14A91D4303	H4301	SWITCHED MODE POWER CONVERSION	33	24	1
14A91D4303	H4303	DIGITAL CONTROLLERS	33	28	1
14A91D4308	H4301	SWITCHED MODE POWER CONVERSION	29	24	1
14A91D4308	H5605	SMART GRID	31	25	1
14A91D4309	H4301	SWITCHED MODE POWER CONVERSION	33	16	0
14A91D4309	H5605	SMART GRID	29	26	1
14A91D5204	H4301	SWITCHED MODE POWER CONVERSION	27	10	0
14A91D5204	H4303	DIGITAL CONTROLLERS	30	28	1
14A91D5205	H4301	SWITCHED MODE POWER CONVERSION	30	16	0
14A91D5206	H4301	SWITCHED MODE POWER CONVERSION	29	6	0
14A91D5506	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	33	19	0
14A91D5509	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	34	17	0
14A91D5510	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	32	36	1
14A91D5511	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	27	17	0
14A91D5511	H6806	SYSTEM ON CHIP DESIGN	28	27	1

Htno	Subcode	Subname	Internal	External	credits
14A91D7223	H6806	SYSTEM ON CHIP DESIGN	32	30	1
14A91D8706	H8701	FINITE ELEMENT METHOD	34	26	1
14A91D8707	H8701	FINITE ELEMENT METHOD	35	25	1
14A91D8707	H8703	STABILITY OF STRUCTURES	30	36	1
14A91D8707	H8709	BRIDGE ENGINEERING ELECTIVE-IV	33	32	1
14A91D8708	H8703	STABILITY OF STRUCTURES	34	31	1
14A91D8709	H8703	STABILITY OF STRUCTURES	34	28	1
14A91D8712	H8709	BRIDGE ENGINEERING ELECTIVE-IV	33	31	1
14A91D8715	H8701	FINITE ELEMENT METHOD	28	27	1
14A91D8715	H8703	STABILITY OF STRUCTURES	27	24	1
15A91D0803	H0801	ADVANCED NATURAL GAS ENGINEERING	32	24	1
15A91D0804	H0801	ADVANCED NATURAL GAS ENGINEERING	32	24	1
15A91D0804	H0802	ARTIFICIAL LIFT TECHNIQUES	32	27	1
15A91D0806	H0801	ADVANCED NATURAL GAS ENGINEERING	37	30	1
15A91D2102	H2104	COMPUTATIONAL FLUID DYNAMICS	27	9	0
15A91D2102	H2107	THERMAL AND NUCLEAR POWER PLANTS ELECTIV	29	31	1
15A91D2102	H2109	THERMAL MEASUREMENTS AND PROCESS CONTROL	33	14	0
15A91D2104	H2104	COMPUTATIONAL FLUID DYNAMICS	26	33	1
15A91D2104	H2109	THERMAL MEASUREMENTS AND PROCESS CONTROL	37	27	1
15A91D2106	H2104	COMPUTATIONAL FLUID DYNAMICS	31	23	0
15A91D2107	H2104	COMPUTATIONAL FLUID DYNAMICS	40	29	1
15A91D2108	H2104	COMPUTATIONAL FLUID DYNAMICS	29	28	1
15A91D4304	H4301	SWITCHED MODE POWER CONVERSION	31	28	1
15A91D5203	H4301	SWITCHED MODE POWER CONVERSION	35	10	0
15A91D5203	H4304	CUSTOM POWER DEVICES	38	27	1
15A91D5501	H5501	HARDWARE SOFTWARE CO-DESIGN	35	30	1
15A91D5501	H5502	EMBEDDED NETWORKING	37	15	0
15A91D5501	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	36	17	0
15A91D5502	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	40	22	0
15A91D5504	H4508	INTERNET PROTOCOLS	30	22	0
15A91D5504	H5501	HARDWARE SOFTWARE CO-DESIGN	30	25	1
15A91D5504	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	36	19	0
15A91D5505	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	37	11	0
15A91D5507	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	35	11	0
15A91D5509	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	35	15	0
15A91D5805	H4002	INFORMATION SECURITY	26	9	0
15A91D7203	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	36	24	1
15A91D7204	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	34	38	1
15A91D7206	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	33	25	1
15A91D7208	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	39	27	1
15A91D8703	H8703	STABILITY OF STRUCTURES	39	32	1
16A91D0801	J0801	ADVANCED NATURAL GAS ENGINEERING	36	26	1
16A91D0801	J0802	ARTIFICIAL LIFT TECHNIQUES	36	29	1
16A91D0801	J0803	OPERATIONAL ASPECTS OF WELL TESTING	36	32	1
16A91D0801	J0804	INTEGRATED RESERVOIR MANAGEMENT	36	32	1
16A91D0801	J0805	PRACTICAL RESERVOIR MODELING & SIMULATIO	34	24	1
16A91D0801	J0808	PROCESS SAFETY & ENVIRONMENTAL ASPECTS I	37	30	1
16A91D0801	J0811	RESERVOIR SIMULATION LAB	38	55	1
16A91D0802	J0801	ADVANCED NATURAL GAS ENGINEERING	31	25	1
16A91D0802	J0802	ARTIFICIAL LIFT TECHNIQUES	32	24	1
16A91D0802	J0803	OPERATIONAL ASPECTS OF WELL TESTING	35	25	1

Htno	Subcode	Subname	Internal	External	credits
16A91D0802	J0804	INTEGRATED RESERVOIR MANAGEMENT	37	26	1
16A91D0802	J0805	PRACTICAL RESERVOIR MODELING & SIMULATIO	34	24	1
16A91D0802	J0808	PROCESS SAFETY & ENVIRONMENTAL ASPECTS I	35	26	1
16A91D0802	J0811	RESERVOIR SIMULATION LAB	35	43	1
16A91D0803	J0801	ADVANCED NATURAL GAS ENGINEERING	34	24	1
16A91D0803	J0802	ARTIFICIAL LIFT TECHNIQUES	31	24	1
16A91D0803	J0803	OPERATIONAL ASPECTS OF WELL TESTING	33	29	1
16A91D0803	J0804	INTEGRATED RESERVOIR MANAGEMENT	34	32	1
16A91D0803	J0805	PRACTICAL RESERVOIR MODELING & SIMULATIO	33	35	1
16A91D0803	J0808	PROCESS SAFETY & ENVIRONMENTAL ASPECTS I	31	35	1
16A91D0803	J0811	RESERVOIR SIMULATION LAB	28	38	1
16A91D2102	J2101	FUELS COMBUSTION & ENVIRONMENT	30	14	0
16A91D2102	J2102	ENERGY MANAGEMENT	40	32	1
16A91D2102	J2103	FINITE ELEMENT METHODS	33	24	1
16A91D2102	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	31	6	0
16A91D2102	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	34	32	1
16A91D2102	J2109	THERMAL MEASUREMENTS AND PROCESS CONTROL	34	24	1
16A91D2102	J2113	THERMAL SYSTEMS DESIGN LAB	36	51	1
16A91D2103	J2101	FUELS COMBUSTION & ENVIRONMENT	33	17	0
16A91D2103	J2102	ENERGY MANAGEMENT	40	40	1
16A91D2103	J2103	FINITE ELEMENT METHODS	34	26	1
16A91D2103	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	31	13	0
16A91D2103	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	36	31	1
16A91D2103	J2109	THERMAL MEASUREMENTS AND PROCESS CONTROL	34	25	1
16A91D2103	J2113	THERMAL SYSTEMS DESIGN LAB	36	53	1
16A91D2104	J2101	FUELS COMBUSTION & ENVIRONMENT	30	15	0
16A91D2104	J2102	ENERGY MANAGEMENT	40	29	1
16A91D2104	J2103	FINITE ELEMENT METHODS	35	16	0
16A91D2104	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	35	18	0
16A91D2104	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	35	24	1
16A91D2104	J2109	THERMAL MEASUREMENTS AND PROCESS CONTROL	36	24	1
16A91D2104	J2113	THERMAL SYSTEMS DESIGN LAB	37	55	1
16A91D2105	J2101	FUELS COMBUSTION & ENVIRONMENT	32	16	0
16A91D2105	J2102	ENERGY MANAGEMENT	40	34	1
16A91D2105	J2103	FINITE ELEMENT METHODS	36	13	0
16A91D2105	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	33	24	1
16A91D2105	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	35	24	1
16A91D2105	J2109	THERMAL MEASUREMENTS AND PROCESS CONTROL	36	30	1
16A91D2105	J2113	THERMAL SYSTEMS DESIGN LAB	36	56	1
16A91D2106	J2101	FUELS COMBUSTION & ENVIRONMENT	29	12	0
16A91D2106	J2102	ENERGY MANAGEMENT	40	34	1
16A91D2106	J2103	FINITE ELEMENT METHODS	34	16	0
16A91D2106	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	31	14	0
16A91D2106	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	35	25	1
16A91D2106	J2109	THERMAL MEASUREMENTS AND PROCESS CONTROL	34	31	1
16A91D2106	J2113	THERMAL SYSTEMS DESIGN LAB	36	51	1
16A91D5201	J4301	SWITCHED MODE POWER CONVERSION	39	39	1
16A91D5201	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	37	24	1
16A91D5201	J4303	DIGITAL CONTROLLERS	39	33	1
16A91D5201	J4304	CUSTOM POWER DEVICES	37	35	1
16A91D5201	J4305	RENEWABLE ENERGY SYSTEMS ELECTIVEIII	39	45	1

Htno	Subcode	Subname	Internal	External	credits
16A91D5201	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	38	42	1
16A91D5201	J4310	POWER CONVERTERS & DRIVES LABORATORY	34	53	1
16A91D5202	J4301	SWITCHED MODE POWER CONVERSION	39	29	1
16A91D5202	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	37	24	1
16A91D5202	J4303	DIGITAL CONTROLLERS	31	25	1
16A91D5202	J4304	CUSTOM POWER DEVICES	35	31	1
16A91D5202	J4305	RENEWABLE ENERGY SYSTEMS ELECTIVEIII	34	31	1
16A91D5202	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	36	33	1
16A91D5202	J4310	POWER CONVERTERS & DRIVES LABORATORY	34	53	1
16A91D5203	J4301	SWITCHED MODE POWER CONVERSION	39	34	1
16A91D5203	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	35	34	1
16A91D5203	J4303	DIGITAL CONTROLLERS	37	14	0
16A91D5203	J4304	CUSTOM POWER DEVICES	39	34	1
16A91D5203	J4305	RENEWABLE ENERGY SYSTEMS ELECTIVEIII	39	34	1
16A91D5203	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	38	41	1
16A91D5203	J4310	POWER CONVERTERS & DRIVES LABORATORY	35	54	1
16A91D5204	J4301	SWITCHED MODE POWER CONVERSION	40	27	1
16A91D5204	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	37	27	1
16A91D5204	J4303	DIGITAL CONTROLLERS	39	12	0
16A91D5204	J4304	CUSTOM POWER DEVICES	37	27	1
16A91D5204	J4305	RENEWABLE ENERGY SYSTEMS ELECTIVEIII	39	30	1
16A91D5204	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	38	32	1
16A91D5204	J4310	POWER CONVERTERS & DRIVES LABORATORY	35	54	1
16A91D5205	J4301	SWITCHED MODE POWER CONVERSION	38	31	1
16A91D5205	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	37	15	0
16A91D5205	J4303	DIGITAL CONTROLLERS	28	24	1
16A91D5205	J4304	CUSTOM POWER DEVICES	35	12	0
16A91D5205	J4305	RENEWABLE ENERGY SYSTEMS ELECTIVEIII	29	30	1
16A91D5205	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	37	25	1
16A91D5205	J4310	POWER CONVERTERS & DRIVES LABORATORY	34	53	1
16A91D5501	J5501	HARDWARE SOFTWARE CODESIGN	27	2	0
16A91D5501	J5502	EMBEDDED NETWORKING	32	24	1
16A91D5501	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	38	58	1
16A91D5501	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	28	3	0
16A91D5501	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	23	9	0
16A91D5501	J6805	DSP PROCESSORS AND ARCHITECTURES	24	19	0
16A91D5501	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	25	13	0
16A91D5502	J5501	HARDWARE SOFTWARE CODESIGN	40	29	1
16A91D5502	J5502	EMBEDDED NETWORKING	40	41	1
16A91D5502	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	38	58	1
16A91D5502	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	38	34	1
16A91D5502	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	38	34	1
16A91D5502	J6805	DSP PROCESSORS AND ARCHITECTURES	40	38	1
16A91D5502	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	26	1
16A91D5503	J5501	HARDWARE SOFTWARE CODESIGN	25	17	0
16A91D5503	J5502	EMBEDDED NETWORKING	28	28	1
16A91D5503	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	36	54	1
16A91D5503	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	27	11	0
16A91D5503	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	28	10	0
16A91D5503	J6805	DSP PROCESSORS AND ARCHITECTURES	29	18	0
16A91D5503	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	30	18	0

Htno	Subcode	Subname	Internal	External	credits
16A91D5504	J5501	HARDWARE SOFTWARE CODESIGN	39	24	1
16A91D5504	J5502	EMBEDDED NETWORKING	40	33	1
16A91D5504	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	35	50	1
16A91D5504	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	39	18	0
16A91D5504	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	37	24	1
16A91D5504	J6805	DSP PROCESSORS AND ARCHITECTURES	35	27	1
16A91D5504	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	32	34	1
16A91D5506	J5501	HARDWARE SOFTWARE CODESIGN	36	24	1
16A91D5506	J5502	EMBEDDED NETWORKING	36	36	1
16A91D5506	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	35	47	1
16A91D5506	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	33	19	0
16A91D5506	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	29	24	1
16A91D5506	J6805	DSP PROCESSORS AND ARCHITECTURES	35	24	1
16A91D5506	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	32	32	1
16A91D5507	J5501	HARDWARE SOFTWARE CODESIGN	35	2	0
16A91D5507	J5502	EMBEDDED NETWORKING	35	31	1
16A91D5507	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	34	50	1
16A91D5507	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	32	17	0
16A91D5507	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	35	24	1
16A91D5507	J6805	DSP PROCESSORS AND ARCHITECTURES	34	11	0
16A91D5507	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	30	31	1
16A91D5801	J0502	SOFTWARE ENGINEERING ELECTIVE I	40	40	1
16A91D5801	J2503	CYBER SECURITY	38	36	1
16A91D5801	J2510	CLOUD COMPUTING ELECTIVE II	39	26	1
16A91D5801	J4001	ADVANCED UNIX PROGRAMMING	40	28	1
16A91D5801	J4002	BIG DATA ANALYTICS	40	47	1
16A91D5801	J5801	COMPUTER NETWORKS	39	39	1
16A91D5801	J5803	CSE LAB 2	39	55	1
16A91D5802	J0502	SOFTWARE ENGINEERING ELECTIVE I	40	36	1
16A91D5802	J2503	CYBER SECURITY	36	35	1
16A91D5802	J2510	CLOUD COMPUTING ELECTIVE II	37	25	1
16A91D5802	J4001	ADVANCED UNIX PROGRAMMING	39	34	1
16A91D5802	J4002	BIG DATA ANALYTICS	29	48	1
16A91D5802	J5801	COMPUTER NETWORKS	39	47	1
16A91D5802	J5803	CSE LAB 2	38	52	1
16A91D5803	J0502	SOFTWARE ENGINEERING ELECTIVE I	26	18	0
16A91D5803	J2503	CYBER SECURITY	30	6	0
16A91D5803	J2510	CLOUD COMPUTING ELECTIVE II	27	9	0
16A91D5803	J4001	ADVANCED UNIX PROGRAMMING	28	8	0
16A91D5803	J4002	BIG DATA ANALYTICS	28	10	0
16A91D5803	J5801	COMPUTER NETWORKS	33	4	0
16A91D5803	J5803	CSE LAB 2	33	40	1
16A91D5804	J0502	SOFTWARE ENGINEERING ELECTIVE I	36	29	1
16A91D5804	J2503	CYBER SECURITY	35	15	0
16A91D5804	J2510	CLOUD COMPUTING ELECTIVE II	30	29	1
16A91D5804	J4001	ADVANCED UNIX PROGRAMMING	35	24	1
16A91D5804	J4002	BIG DATA ANALYTICS	36	6	0
16A91D5804	J5801	COMPUTER NETWORKS	39	27	1
16A91D5804	J5803	CSE LAB 2	38	48	1
16A91D5805	J0502	SOFTWARE ENGINEERING ELECTIVE I	35	31	1
16A91D5805	J2503	CYBER SECURITY	38	30	1

Htno	Subcode	Subname	Internal	External	credits
16A91D5805	J2510	CLOUD COMPUTING ELECTIVE II	33	28	1
16A91D5805	J4001	ADVANCED UNIX PROGRAMMING	33	24	1
16A91D5805	J4002	BIG DATA ANALYTICS	29	33	1
16A91D5805	J5801	COMPUTER NETWORKS	39	15	0
16A91D5805	J5803	CSE LAB 2	30	42	1
16A91D7201	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	38	29	1
16A91D7201	J5704	BACK END VLSI DESIGN LABORATORY	34	51	1
16A91D7201	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	35	47	1
16A91D7201	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	35	35	1
16A91D7201	J6804	DESIGN FOR TESTABILITY	35	30	1
16A91D7201	J6805	DSP PROCESSORS AND ARCHITECTURES	32	40	1
16A91D7201	J6806	LOW POWER VLSI DESIGN	34	36	1
16A91D7202	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	34	33	1
16A91D7202	J5704	BACK END VLSI DESIGN LABORATORY	35	53	1
16A91D7202	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	37	40	1
16A91D7202	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	36	41	1
16A91D7202	J6804	DESIGN FOR TESTABILITY	36	15	0
16A91D7202	J6805	DSP PROCESSORS AND ARCHITECTURES	39	24	1
16A91D7202	J6806	LOW POWER VLSI DESIGN	35	38	1
16A91D7203	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	37	26	1
16A91D7203	J5704	BACK END VLSI DESIGN LABORATORY	36	55	1
16A91D7203	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	39	45	1
16A91D7203	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	37	31	1
16A91D7203	J6804	DESIGN FOR TESTABILITY	37	30	1
16A91D7203	J6805	DSP PROCESSORS AND ARCHITECTURES	39	47	1
16A91D7203	J6806	LOW POWER VLSI DESIGN	38	40	1
16A91D8701	J8701	FINITE ELEMENT METHODS	32	1	0
16A91D8701	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	36	24	1
16A91D8701	J8703	STABILITY OF STRUCTURES	36	21	0
16A91D8701	J8704	THEORY OF PLATES & SHELLS	37	1	0
16A91D8701	J8705	PRESTRESSED CONCRETE ELECTIVEI	37	0	0
16A91D8701	J8709	BRIDGE ENGINEERING ELECTIVEII	37	25	1
16A91D8701	J8711	CAD LABORATORY	36	52	1
16A91D8703	J8701	FINITE ELEMENT METHODS	33	46	1
16A91D8703	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	37	28	1
16A91D8703	J8703	STABILITY OF STRUCTURES	37	47	1
16A91D8703	J8704	THEORY OF PLATES & SHELLS	38	31	1
16A91D8703	J8705	PRESTRESSED CONCRETE ELECTIVEI	38	16	0
16A91D8703	J8709	BRIDGE ENGINEERING ELECTIVEII	38	27	1
16A91D8703	J8711	CAD LABORATORY	38	55	1
16A91D8704	J8701	FINITE ELEMENT METHODS	33	29	1
16A91D8704	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	36	25	1
16A91D8704	J8703	STABILITY OF STRUCTURES	36	24	1
16A91D8704	J8704	THEORY OF PLATES & SHELLS	37	13	0
16A91D8704	J8705	PRESTRESSED CONCRETE ELECTIVEI	38	12	0
16A91D8704	J8709	BRIDGE ENGINEERING ELECTIVEII	37	33	1
16A91D8704	J8711	CAD LABORATORY	36	52	1
16A91D8705	J8701	FINITE ELEMENT METHODS	33	39	1
16A91D8705	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	37	32	1
16A91D8705	J8703	STABILITY OF STRUCTURES	37	35	1
16A91D8705	J8704	THEORY OF PLATES & SHELLS	38	32	1

Htno	Subcode	Subname	Internal	External	credits
16A91D8705	J8705	PRESTRESSED CONCRETE ELECTIVEI	37	16	0
16A91D8705	J8709	BRIDGE ENGINEERING ELECTIVEII	38	26	1
16A91D8705	J8711	CAD LABORATORY	37	54	1

****Note:1)**For Recounting/Revaluation/Challenge By Revaluation Apply through Online(www.jntukresults.edu.in))

****NOTE:2** [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **27-12-2017**]

****NOTE:3** [Please inform to the students to enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation]

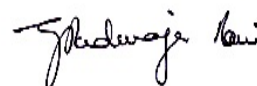
****NOTE:**

[-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject.]

Date:06-12-2017



Controller of Examinations